**EXPERIMENT 10 FREQUENCY RESPONSE AND BODE PLOTS**

**DATE:**

For the RC low pass filter(integrator) circuit shown in figure, obtain the transfer function and write a program to obtain the frequency response(steady state response) of the circuit by plotting magnitude and phase plots (also called bode plots) for given values of R and C on semilog graph. The magnitude plot must have magnitude in dB. Use the inbuilt functions to generate and plot frequency response (sys and bode or freqs and semilogx). Verify cut-off frequency from the graph.





Here,

and